



International Workshop on



Advanced Patterning Solutions

## The 9<sup>th</sup> International Workshop on Advanced Patterning Solutions

第九届国际先进光刻技术研讨会

*October 14-15, 2025, WuZhou Hotel, Shenzhen, Guangdong Province, China*

2025 年 10 月 14 日至 15 日，五洲宾馆五洲厅，广东深圳，中国

(October 13 for registration, 10 月 13 日注册)

## Agenda 会议日程

Program Chairs: Qiang Wu, Shiyuan Liu

<b>Registration 注册</b>		
<b>13 Oct. 2025</b>	<b>10:30-20:00</b>	酒店大厅 WuZhou Hotel
<b>14-15 Oct. 2025</b>	<b>08:00-18:00</b>	五洲厅 Wuzhou Banquet Hall
<b>DAY 1:</b>		
<b>14 Oct. 2025 (Tuesday)</b>		
<b>Wuzhou Banquet Hall 五洲厅</b>		
<b>DAY 1-Morning</b>		
<b>08:30-09:00</b>	<b>Opening Ceremony &amp; Welcome Address</b> <b>Chair: Yayi Wei</b>	
<b>Welcome Address</b>	<b>Conference Chairs Address</b> <b>Host/Organizer Address</b>	
<b>09:00-10:10</b>	<b>Plenary Session I</b> <b>Chair: Shiyuan Liu</b>	
	<i>5 minutes Q&amp;A for each talk</i>	
<b>09:00-09:35</b>	<b>Linyong(Leo) Pang (D2S, Inc):</b> (KEYNOTE) The Most Effective Way to Advance Technology Nodes – Even Mature Ones: Production-Ready Full-Chip Curvilinear ILT and Curvilinear Masks for DUV	
<b>09:35-10:10</b>	<b>Qiang Wu (Fudan University):</b> (KEYNOTE) DICO in Times of Large Scale Process, Equipment, and Material Development	
<b>10:10-10:40</b>	<b>Group Photo &amp; Coffee Break</b>	
<b>10:40-11:50</b>	<b>Plenary Session II</b> <b>Chair: Qiang Wu</b>	

	<i>5 minutes Q&amp;A for each talk</i>
10:40-11:15	<b>Bei Yu (The Chinese University of Hong Kong):</b> (KEYNOTE) Large Scale VLSI Mask Optimization
11:15-11:50	<b>ByoungHo Lee (Hitachi High-tech Corporation):</b> (KEYNOTE) The Role and Direction of MI in the Era of 3D
<b>11:50-12:20</b>	<b>Poster Session</b> Authors should be present at your poster.
<b>12:20-13:50</b>	<b>Lunch</b>
<b>DAY 1-Afternoon</b>	
<b>13:50-15:30</b>	<b>Patterning Session</b> <b>Chairs: Wenzhan Zhou &amp; Jin Li</b>
	<i>5 minutes Q&amp;A for each talk</i>
13:50-14:15	<b>Jette van den Broeke (ASML):</b> (INVITED) Printing towards the resolution limit of DUV immersion lithography
14:15-14:40	<b>Ingo Bork (Siemens):</b> (INVITED) Practical solutions for writing curvilinear shapes on photomasks
14:40-15:05	<b>Chin-Chou Kevin Huang (KLA):</b> (INVITED) From Metrology to Control: A Comprehensive Overlay Solution for IC Manufacturing and Packaging Challenges
15:05-15:30	<b>Lei Zhu (Wintech-Nano Co., Ltd.):</b> (INVITED) Application of failure analytical techniques for photoresist and photomask defects
<b>15:30-15:50</b>	<b>Coffee Break</b>
<b>15:50-17:50</b>	<b>AI and Process Session</b> <b>Chairs: Weimin Gao &amp; Liguang Zhang</b>
	<i>5 minutes Q&amp;A for each talk</i>
15:50-16:15	<b>Kan Zhou (HLMC):</b> (INVITED) AI in Wafer Manufacturing: Applications and Implementation Pathways
16:15-16:40	<b>Yelin Hu (Engitist Corporation):</b> (INVITED) Machine Learning and Artificial Intelligence in Semiconductor Metrology and Inspection for Smart Manufacturing
16:40-17:05	<b>Ao Chen (SEIDA Technology Co. Ltd.):</b> (INVITED) AI Assisted Etch Models
17:05-17:30	<b>Xianhe Liu (Fudan University):</b> (INVITED) Study of SMO for High-NA EUV Lithography via Sub-2

	nm Nodes
17:30-17:50	<b>Devin Sima (FUJIAN JINHUA INTEGRATED CIRCUIT CO., LTD.):</b> A composite model for LELE process
19:00-19:05	<b>Best Poster Award 最佳墙报颁奖</b> <b>Wuzhou Banquet Hall 五洲厅</b>
19:05-21:00	<b>Welcome Banquet for all attendees</b> <b>晚宴 (Wuzhou Banquet Hall 五洲厅)</b>

<b>Day 2:</b>	
<b>15 Oct. 2025 (Wednesday) — Parallel Session I, 并行报告会场 I</b>	
<b>Wuzhou Banquet Hall Part A, 五洲厅 A</b>	
<b>DAY 2-Morning</b>	
<b>08:30-10:05</b>	<b>Mask Equipment Session</b> <b>Chairs: Jiangliu Shi &amp; Modern Xu</b>
	<i>5 minutes Q&amp;A for each talk</i>
08:30-08:55	<b>Robert Eklund (Mycronic AB):</b> (INVITED) The Laser mask writer and its unique position in the semicon industry
08:55-09:20	<b>Sebastian Vollmar (Carl Zeiss SMT):</b> (INVITED) Advancing E-Beam Repair: Unveiling the MeRiT® MG neo's Enhanced Throughput and Expanded Capabilities
09:20-09:45	<b>Ya Xu (SUSS MicroTec (Shanghai) Co., Ltd):</b> (INVITED) Development of Sulfate and Ammonium Free Resist Stripping and Final Cleaning Process
09:45-10:05	<b>Weiyu Xu (Carl Zeiss SMT):</b> Mask yield accelerator: AIMS AutoAnalysis application to high volume manufacturing
<b>10:05-10:30</b>	<b>Coffee Break</b>
<b>10:30-12:20</b>	<b>Mask Optimization Session</b> <b>Chairs: Yijiang Shen &amp; Haizhou Yin</b>
	<i>5 minutes Q&amp;A for each talk</i>
10:30-10:55	<b>Haizhou Yin (Siemens):</b> (INVITED) Accelerating Semiconductor Innovation: ML/AI Solutions for Mask Synthesis and Beyond
10:55-11:20	<b>Jichuan Xiong (Nanjing University of Science and Technolog):</b> (INVITED) Phase-Space-Driven ILT with Swin Transformers: A Generative AI Framework for Sub-Wavelength Mask Optimization
11:20-11:40	<b>Juan Wei (Beijing Superstring Academy of Memory Technology):</b> Enhancing ILT Modeling Efficiency: Symmetry-Averaged Gauge Processing and Kernel DDM for RMS and Runtime Optimization

11:40-12:00	<b>Yuhang Wang (Guangdong University of Technology):</b> Parametric Curvilinear OPC Using B-splines Data Format
12:00-12:20	<b>Shi Chen (Fudan University):</b> M3D Effect Compensation in 2 nm Node EUV Lithography via Controlled Aberration Manipulation
<b>12:20-13:50</b>	<b>Lunch</b>
<b>DAY 2-Afternoon</b>	
<b>13:50-15:25</b>	<b>Equipment Session</b> <b>Chairs: Yun Zhan &amp; Jing Li</b>
	<i>5 minutes Q&amp;A for each talk</i>
13:50-14:15	<b>Yoshiaki Yamada (Hangzhou Cobetter Filtration Equipment Co. Ltd.):</b> (INVITED) Next-Generation Polyethylene Membrane with Ultra-Fine Pores Beyond Sub-1nm for Lithography Chemical Filtration
14:15-14:40	<b>Jie Liu (ASML Cymer):</b> (INVITED) Maximizing System Performance to Improve Process, Availability and Sustainability
14:40-15:05	<b>Yang Liu (HIT):</b> (INVITED) Modeling and Control of Linear Reluctance Actuators for Next-Generation Wafer Scanners
15:05-15:25	<b>Jerry Wu (Shanghai Jheat Technology Co., Ltd.):</b> An In-wafer System: In-situ Scanner Temp System
<b>15:25-15:45</b>	<b>Coffee Break</b>
<b>15:45-17:15</b>	<b>Other Lithography Session</b> <b>Chairs: Shisheng Xiong &amp; Yang Liu</b>
	<i>5 minutes Q&amp;A for each talk</i>
15:45-16:10	<b>Qiming Zhang (University of Shanghai for Science and Technology):</b> (INVITED) Artificial neural networks enabled by direct laser writing
16:10-16:35	<b>Xing Cheng (Southern University of Science and Technology):</b> (INVITED) Opportunities and Perspectives of Nanoimprint for Commercial Microelectronic Manufacturing
16:35-16:55	<b>Chenhui Deng (Institute of Electrical Engineering, CAS):</b> High-speed versatile pattern generator for high-resolution electron-beam lithography
16:55-17:15	<b>Xin Zhuang (Southern University of Science and Technology):</b> Scanning Helium Ion Beam Lithography on Freestanding Silicon Nitride Membranes for Sub-Nanometer Resist Characterization
<b>17:15-17:20</b>	<b>Closing Plenary Address 闭幕致辞</b>

	<b>Chairs: Qiang Wu, Shiyuan Liu &amp; Yayi Wei</b>
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<b>Day 2:</b>	
<b>15 Oct. 2025 (Wednesday) — Parallel Session II, 并行报告会场 II</b>	
<b>Wuzhou Banquet Hall Part B, 五洲厅 B</b>	
<b>DAY 2–Morning</b>	
<b>08:30-10:15</b>	<b>Computational Lithography Session</b> <b>Chairs: Xu Ma &amp; Xiaodong Meng</b>
	<i>5 minutes Q&amp;A for each talk</i>
08:30-08:55	<b>David H. Wei (Guangdong University of Technology):</b> (INVITED) Computational Imaging and Computational Lithography
08:55-09:15	<b>Cai Chen (Advanced Manufacturing EDA Co., Ltd.):</b> Enhancing Lithographic Resolution for Via and Metal Layers in Advanced Semiconductor Manufacturing: NTD-Based Multi-Patterning Strategies
09:15-09:35	<b>Yuhao Wang (Hangzhou HFC Semiconductor Corp):</b> D-CAF: Data-Driven CPU Allocation Framework for Efficient OPC Workloads
09:35-09:55	<b>Honglan Shao (Shanghai Huali Integrated Circuit Corp):</b> An OPC model for predicting HMO defects
09:55-10:15	<b>Chuansheng Dai (Advanced Manufacturing EDA Co., Ltd):</b> 3D resist profile simulation for compact modeling considering z-diffusion
<b>10:15-10:35</b>	<b>Coffee Break</b>
<b>10:35-12:25</b>	<b>Computational Lithography Session II</b> <b>Chairs: David H. Wei &amp; Sikun Li</b>
	<i>5 minutes Q&amp;A for each talk</i>
10:35-11:00	<b>Yanli Li (Fudan University):</b> (INVITED) Imaging Characteristics of Hyper NA (0.75 NA) EUV Lithography in sub-2 nm Logic Technology Nodes
11:00-11:25	<b>Jing Fu (Shanghai Silicon Thread Technology Co., Ltd):</b> (INVITED) AI-Assisted Hotspot-Aware Layout Correction for Lithography-Friendly DTCO
11:25-11:45	<b>Ningmu Zou (Nanjing University):</b> SVD-based Layout Representation for Lithographic Hotspot Detection
11:45-12:05	<b>Kaisheng Chen (Shanghai Optical Lithography Engineering Corp.):</b> Fraunhofer diffraction by rectangular patterns on mask: a generic diffraction formula for modeling perspective
12:05-12:25	<b>Haofeng Guo (Changchun Institute of Optics, Fine Mechanics and Physics, CAS):</b> A low-memory-consumption lithography imaging model based on

	the Runge-Kutta Iterative Solution (RKIS) method
<b>12:25-13:50</b>	<b>Lunch</b>
<b>DAY 2-Afternoon</b>	
<b>13:50-15:20</b>	<b>X-ray Source and Metrology Session</b> <b>Chairs: Qiang Wu &amp; Kan Zhou</b>
	<i>5 minutes Q&amp;A for each talk</i>
13:50-14:15	<b>Lifeng Wang (Institute of Advanced Light Source Facilities):</b> (INVITED) High order harmonic generation in capillary driven by few-cycle laser pulses
14:15-14:40	<b>Guangcai Chang (Institute of High Energy Physics, CAS):</b> (INVITED) Hard X-ray Nanoprobe Beamline of a new synchrotron light source - High Energy Photon Source (HEPS)
14:40-15:00	<b>Yinqiao Feng (Shenzhen Angstrom Excellence Technology Co. Ltd):</b> IGZO Inline Monitoring Tool for 3D DRAM Performance Control
15:00-15:20	<b>Shuzhe Cao (Zhejiang University):</b> Coating Defect Localization via Patch-Guided Generative Adversarial Networks
<b>15:20-15:40</b>	<b>Coffee Break</b>
<b>15:40-17:10</b>	<b>Process and Metrology Session</b> <b>Chairs: Weijie Shi &amp; Xianhe Liu</b>
	<i>5 minutes Q&amp;A for each talk</i>
15:40-16:05	<b>Taekwon Jee (SemiAI Co. Ltd.):</b> (INVITED) Human-in-the-Loop Intelligent Yield Enhancement in Photolithography via AI-Driven Root Cause Analysis
16:05-16:30	<b>YiFei Zhu (Huali Microelectronics Corporation):</b> (INVITED) Cross-Product Overlay Fingerprint Prediction Using Term-Level Virtual Metrology and Neural Networks
16:30-16:50	<b>Ganlin Song (Beijing Superstring Academy of Memory Technology):</b> Innovative Topographic Mark Generation and Process Optimization for Enhancing Overlay Mark Signal in 3D Structures
16:50-17:10	<b>Zhengpeng Zhang (Fudan University):</b> Optimization of Process Parameters to Reduce Photon Absorption Stochastics for Bidirectional Patterning with EUV Single Exposure
<b>17:10-17:15</b>	<b>Closing Plenary Address 闭幕致辞</b> <b>Chairs: Qiang Wu, Shiyuan Liu &amp; Yayi Wei</b>

<b>Day 2:</b> <b>15 Oct. 2025 (Wednesday) — Parallel Session III, 并行报告会场 III</b> <b>Wuzhou Banquet Hall Part C, 五洲厅 C</b>	
<b>DAY 2–Morning</b>	
<b>08:30-10:00</b>	<b>X-ray Metrology Session</b> <b>Chairs: Feng Luo &amp; Ruzhi Zhang</b>
	<i>5 minutes Q&amp;A for each talk</i>
08:30-08:55	<b>Yasin Ekinici (Paul Scherrer Institute):</b> (INVITED) EUV metrology methods for semiconductor manufacturing applications
08:55-09:20	<b>Jun Zhao (Shanghai Advanced Research Institute):</b> (INVITED) Soft X-ray interference lithography and its applications
09:20-09:40	<b>Jiahao Zhang (Huazhong University of Science and Technology):</b> X-ray critical dimension metrology with optimal experimental design using the Fisher information
09:40-10:00	<b>Yuxiang Huang (Shenzhen Angstrom Excellence Technology Co. Ltd):</b> Semiconductor Metrology Tool: X-Ray Diffraction, X-Ray Reflectivity and X-Ray Fluorescence Techniques
<b>10:00-10:20</b>	<b>Coffee Break</b>
<b>10:20-12:10</b>	<b>Photoresist Session I</b> <b>Chairs: Guoqiang Yang &amp; Dean Wu</b>
	<i>5 minutes Q&amp;A for each talk</i>
10:20-10:45	<b>Feng Luo (Nankai University):</b> (INVITED) EUV Photoresist for Advanced 0.55NA Lithography
10:45-11:10	<b>Hideaki Tsubaki (FUJIFILM):</b> (INVITED) Negative-Tone-Imaging (NTI) Process and Material Technology for ArF Immersion and EUV Lithography
11:10-11:30	<b>Ruiwen Ai (Xuzhou B&amp;C Chemical Co.,Ltd):</b> A Feasible Approach to Understand Acid Diffusion in DUV Resist Using Open Frame Exposure
11:30-11:50	<b>Yanjiao Li (Shanghai Huali Microelectronics Corporation):</b> Source-only Optimization Method to adjust Freeform Source for Improved Photoresist Matching
11:50-12:10	<b>Yi-Fan Shen (Suzhou Laboratory):</b> Acid Diffusion Detection in Chemically Amplified Photoresists
<b>12:10-13:50</b>	<b>Lunch</b>
<b>DAY 2–Afternoon</b>	
<b>13:50-15:25</b>	<b>Photoresist Session II</b> <b>Chairs: Mark Neisser &amp; Bing Li</b>

	<i>5 minutes Q&amp;A for each talk</i>
13:50-14:15	<b>Ruzhi Zhang (Suzhou Laboratory):</b> (INVITED) Molecular Feature Influence on ArF Photoresist Polymer Dissolution Behavior
14:15-14:40	<b>Lei Zhang (Nankai University):</b> (INVITED) Molecular Engineering of Metal-Oxo Clusters for Advanced Patterning Applications
14:40-15:05	<b>Yang Hu (Tsing-innovation Semiconductor Company):</b> (INVITED) MOR photoresists: the next generation pattering solutions for Moore's more
15:05-15:25	<b>Zhongmei Han (PiBond Oy):</b> Metal oxides and organometal complexes as alternatives for lithography hard masks
<b>15:25-15:45</b>	<b>Coffee Break</b>
<b>15:45-17:10</b>	<b>Photoresist and Material Session</b> <b>Chair: Xudong Guo</b>
	<i>5 minutes Q&amp;A for each talk</i>
15:45-16:10	<b>Yu Chen (Suzhou Rainbowmaterial Co., Ltd.):</b> (INVITED) High etching ration ArF immersion BARC to improve advanced lithographs technology
16:10-16:30	<b>Ruzhi Zhang (Suzhou Laboratory):</b> Development of High-Performance NTD Developers via HSP-Assisted HTE
16:30-16:50	<b>Xiang Dong (Shanghai B&amp;C Chemical Co., Ltd.):</b> Application and Advantage of RAFT Polymerization in High-resolution Lithography Formulation
16:50-17:10	<b>Jialin Wu (Fudan University):</b> A Hierarchical Pipeline for Data-Efficient $\chi$ Prediction in Block Copolymers via Large Language Models
<b>17:10-17:15</b>	<b>Closing Plenary Address 闭幕致辞</b> <b>Chairs: Qiang Wu, Shiyuan Liu &amp; Yayi Wei</b>

Agenda is subject to change

**Poster Session****14 Oct. 2025****11:50-12:20 Outdoor of Wuzhou Banquet Hall 五洲厅前廊**

IWAPS2025-P-01	<b>Kang Wang*</b> , ZhaoLong Luo, Shiqi Yin (Nexchip Semiconductor Corporation) Full-Field Contour-Based PWA Enables Precision Depth-of-Focus Characterization at Advanced-Node
IWAPS2025-P-02	<b>Kang Wang*</b> , Zhaolong Luo, Die Liu (Nexchip Semiconductor Corporation) Smart RBAF: Hierarchical Main Pattern Definition for Advanced-Node Lithography Enhancement
IWAPS2025-P-03	<b>Tong Liang, Jing Li*</b> (Zhejiang University) A measurement scheme for beam parallelism based on image measurement method
IWAPS2025-P-04	<b>Chao Long<sup>1,2</sup>, Jiantao Wang<sup>2,*</sup>, Jingxun Fang<sup>2</sup>, Yu Zhang<sup>2</sup></b> ( <sup>1</sup> Shanghai University; <sup>2</sup> Shanghai Huali Integrated Circuit Corporation ) A study of photoresist residue defect induced by wafer surface condition
IWAPS2025-P-05	<b>Caiwei Shang<sup>1</sup>, Jiacheng Luo<sup>1</sup>, Weixuan Zeng<sup>2</sup>, Yan Zhang<sup>2</sup>, Shisheng Xiong<sup>1,2,*</sup></b> ( <sup>1</sup> Fudan University; <sup>2</sup> Zhangjiang Laboratory) Fast Simulation of Directed Self-Assembly for Contact Hole Multiplication with an Optimized Compact Model
IWAPS2025-P-06	<b>Xin Zhou<sup>1</sup>, Yuejing Qi<sup>1,2,*</sup>, Zhipeng Wu<sup>1,2</sup>, Wei Qi<sup>1,2</sup></b> ( <sup>1</sup> Institute of Microelectronics of the Chinese Academy of Sciences; <sup>2</sup> University of Chinese Academy of Sciences) A Moving-average-filter-based Dynamic Decoupling Control for Wafer Stage Motion Systems
IWAPS2025-P-07	<b>Yanbei Nan, Zhenkun Zhang, Yanqiu Li*</b> (Beijing Institute of Technology) Design of an anamorphic extreme ultraviolet lithography projection objective adopted high order XY polynomial freeform surfaces
IWAPS2025-P-08	<b>Weichen Huang, Yangqiu Li*</b> (Beijing Institute of Technology) High-fidelity curvilinear SRAF method adopting distance-versus-angle-signature
IWAPS2025-P-09	<b>Zhenkun Zhang, Yanbei Nan, Yanqiu Li*</b> (Beijing Institute of Technology) Wavefront Fitting of Different Obscuration Shapes in Anamorphic Extreme Ultraviolet Lithography Projection Objective
IWAPS2025-P-10	<b>Zhilong Zhong<sup>1</sup>, Yunhao Liu<sup>1</sup>, Jiamin Liu<sup>1,*</sup>, Pinxuan He<sup>1</sup>, Hao Jiang<sup>1</sup>, Honggang Gu<sup>1</sup>, Shiyuan Liu<sup>1,2,*</sup></b> ( <sup>1</sup> Huazhong University of Science and Technology; <sup>2</sup> Optics Valley Laboratory) Full-chip EUVL simulation based on wide-angle full-vector beam propagation method
IWAPS2025-	<b>CHIH-LI(Julius) Chen<sup>1</sup>, Zekui Li<sup>1</sup>, Alex Fan<sup>3</sup>, Dawen Yang<sup>3</sup>,</b>

P-11	<b>Pinhong Lin<sup>3</sup>, Lei Chen<sup>3</sup>, Xiaodong Meng<sup>2,3,*</sup> (1 Rong Semiconductor Co., Ltd.; 2 Tsinghua University; 3 Advanced Manufacturing EDA Co., Ltd.)</b> An effective methodology to accelerate early stage of OPC development by use of TVeXpert and CPG
IWAPS2025-P-12	<b>Benjamin Shen*, Wenchun Huang, Chunhung Wu (Advanced Manufacturing EDA Co., Ltd.)</b> OPC Consistency Improvement with Hash Code
IWAPS2025-P-13	<b>Qinghua Ye<sup>1,2</sup>, Dongchao Pan<sup>2</sup>, Chun Zhang<sup>2</sup>, Diyu Fu, Sikun Li<sup>1,2,*</sup> (1 Shanghai University; 2 Shanghai Institute of Optics and Fine Mechanics, Chinese Academy of Sciences)</b> Real-time Shack-Hartmann Wavefront Processor with FPGA Acceleration
IWAPS2025-P-14	<b>Chaoxin Feng<sup>1</sup>, Cheng Qian<sup>2</sup>, Ruoran Jia<sup>3,*</sup> (1 University of Chinese Academy of Sciences; 2 University of Science and Technology of China; 3 Chongqing Institute of Green and Intelligent Technology, Chinese Academy of Sciences)</b> An effective mask optimization method using Deep Reinforcement Learning
IWAPS2025-P-15	<b>Lingjie Li, Yichi Pan, Haokang Zhang, Wei Liang, Chong Shen*, Dekun Yang* (Hainan University)</b> learning-assisted angle-resolved scatterometry of 3D NAND channel hole structure
IWAPS2025-P-16	<b>Pinxuan He<sup>1</sup>, Jiamin Liu<sup>1,*</sup>, Jinlong Zhu<sup>1</sup>, Honggang Gu<sup>1</sup>, Hao Jiang<sup>1</sup>, Shiyuan Liu<sup>1,2,*</sup> (1 Huazhong University of Science and Technology; 2 Optics Valley Laboratory)</b> Large-scale electromagnetic simulations enabled by the modified Born series with virtual absorbing boundaries
IWAPS2025-P-17	<b>Yingxiong Guo<sup>1</sup>, Wenzhang Li<sup>1</sup>, T. H. Ying<sup>1</sup>, Devin Sima<sup>1</sup>, Siqi Wang<sup>2</sup>, Shengrui Zhang<sup>2</sup>, C. M. Hu<sup>1,*</sup> (1 Fujian Jinhua Integrated Circuit Co., Ltd.; 2 Dongfang Jingyuan Electron Co., Ltd.)</b> A Comprehensive intra-CD uniformity control through optimizing pattern loading effect correction by AI-trained model
IWAPS2025-P-18	<b>Tatsi IAO<sup>1</sup>, Fengnien Tsai<sup>1</sup>, T.H.Ying<sup>1</sup>, Shengrui.Zhang<sup>2</sup>, C.M. Hu<sup>1,*</sup> (1 Fujian Jinhua Integrated Circuit Co., Ltd.; 2 Dongfang Jingyuan Electron Co., Ltd.)</b> A Co-optimization Flow to Expanding PWQ through HSF and Model-based loading effect for DRAM LELE Metal Layer
IWAPS2025-P-19	<b>Yanting Xu<sup>1</sup>, Jiacheng Luo<sup>2</sup>, Zhiyong Wu<sup>1</sup>, Wenda Bao<sup>1</sup>, Yudan Su<sup>1</sup>, Yan Zhang<sup>1</sup>, Shisheng Xiong<sup>1,2,*</sup> (1 Zhangjiang laboratory; 2 Fudan University)</b> Quantitative Characterization of Brushes Grafting for Graphoepitaxial Directed Self-assembly
IWAPS2025-P-20	<b>Yanzhong Ma*, Xiangyu Ma*, Langfeng Wen (Skyverse Tech. Co. Ltd.)</b>

	Synalib: A fast OCD engine with parametric process flow modeling for high-throughput semiconductor metrology
IWAPS2025-P-21	<b>Haoyang Liang<sup>1</sup>, Honggang Gu<sup>1,2,*</sup>, Shiyuan Liu<sup>1,2,*</sup></b> ( <sup>1</sup> Huazhong University of Science and Technology; <sup>2</sup> Optics Valley Laboratory) Sub-wavelength scale lithographic defect detection based on vortex light
IWAPS2025-P-22	<b>Zhiyong Wu<sup>1</sup>, Qingshu Dong<sup>2</sup>, Huangyan Shen<sup>2</sup>, Shengru Niu<sup>2</sup>, Yan Zhang<sup>1</sup>, Fei Pei<sup>1</sup>, Weixuan Zeng<sup>1</sup>, Zhonghan Cao<sup>1</sup>, Mingkun Zhao<sup>1</sup>, Zili Li<sup>1,2</sup>, Weihua Li<sup>2</sup>, Shisheng Xiong<sup>1,2,*</sup></b> ( <sup>1</sup> Zhangjiang Laboratory; <sup>2</sup> Fudan University;) Simultaneous Generation of Contact Holes with Circular and Rectangular Shapes through Graphoepitaxy Directed Self-Assembly of Block Copolymer/Homopolymer Blends
IWAPS2025-P-23	<b>Hui Zeng <sup>1*</sup>, Libin Zhang<sup>2</sup>, Zhicheng Liu<sup>1</sup>, Zhenguo Tian<sup>2</sup>, Guiqi Li <sup>1</sup>, Fangyi Shi <sup>1</sup>, Zhuohong Zhou <sup>1</sup>, Miaohua Zhang <sup>1</sup>, Xing Yang <sup>3</sup></b> ( <sup>1</sup> CanSemi Technology Inc., <sup>2</sup> Institute of Microelectronics, <sup>3</sup> Nanjing Chengxin Integrated Circuit Technology Research Institute Co., Ltd.) Critical Dimension Measurement Method for Curvilinear Patterns and Application in Lithography Process Development for Silicon Photonics Platform
IWAPS2025-P-24	<b>Chenpeng Zhao, Jazlyn Zhang, Pei Xie, Can Duan</b> (Semiconductor Technology Innovation Center (STIC)) Optical Diameter-Driven Optimization of Optical Proximity Correction (OPC) Model Fidelity and Computational Efficiency
IWAPS2025-P-25	<b>Di Liang, Miao Jiang, Yaokun Li, Yuxing Zhou, Jiahao Xi, Enqiang Tian, Mingyi Yao, Ganlin Song, Jiangliu Shi, Guowei Jiang*</b> (Beijing Superstring Academy of Memory Technology) An Open KLayout-Python-Based Framework for Automated AIM Overlay Mark Generation
IWAPS2025-P-26	<b>Jun Zhou, Lei Cheng, Lei Zhu, Yuheng Huang, QingJun Ding, Chengyu Li</b> (Wuhan Xinxin Semiconductor Manufacturing Co., Ltd) Resolution enhancement technology of contact hole patterning by negative tone development of ArF immersion lithography
IWAPS2025-P-27	<b>Sicong Wang<sup>1</sup>, Xiaoqi Pang<sup>2</sup>, Pei Yu<sup>2</sup>, Chao Shang<sup>2</sup>, Zhiwei Wang<sup>3</sup>, Xianhui Chen<sup>3</sup>, Bowen Chen<sup>4</sup>, Yi Yin<sup>4</sup>, Xia Chen<sup>5</sup>, Jie Zhang<sup>5</sup>, Trina Wong<sup>2</sup>, Feng Hong<sup>2</sup>, Athena Chang<sup>2</sup></b> ( <sup>1</sup> Huazhong University of Science and Technology; <sup>2</sup> Shenzhen Angstrom Excellence Technology Co. Ltd.; <sup>3</sup> University of Science and Technology of China; <sup>4</sup> Zhejiang University; <sup>5</sup> Shenzhen Institute of Advanced Technology, Chinese Academy of Sciences) Thin Film Metrology Techniques: X-Ray Diffraction and X-Ray Reflectivity
IWAPS2025-P-28	<b>Chao Liu, Jing Li</b> (Zhejiang University) Polarization-Induced Angular Deviation and Spatial Centroid Jumps in Laser Beams

IWAPS2025-P-29	<b>Li Xiao*, Yueyu Zhang, Shirui Yu (Shanghai Huali Integrated Circuit Corporation)</b> Research on Mask Template Cross Validation Method
IWAPS2025-P-30	<b>Jinyuan Song, Jing Li*, Qingyang Zhang (Zhejiang University)</b> Apparatus and Method for Testing the Optical Uniformity of Optical Crystals
IWAPS2025-P-31	<b>Jingyu Huang<sup>1,2</sup>, Bohua Yin<sup>1,2</sup>, Zhengjie Li<sup>1,2</sup>, Botong Sun<sup>1,2</sup>, Pengfei Wang<sup>1,2</sup>, Li Han<sup>1,2</sup>, Liping Zhang<sup>3</sup></b> ( <sup>1</sup> Institute of Electrical Engineering Chinese Academy of Sciences; <sup>2</sup> University of Chinese Academy of Sciences; <sup>3</sup> Beijing Academy of Quantum Information Sciences) Process Window and Resolution Limit Investigation of PMMA Resist under 30 kV Electron Beam Lithography
IWAPS2025-P-32	<b>Jiakun Lan<sup>1</sup>, Jibin He<sup>1</sup>, Xue Zhou<sup>1</sup>, Yanan Bao<sup>1,2</sup>, Guodong Zhou<sup>1,*</sup></b> ( <sup>1</sup> Zhejiang University; <sup>2</sup> Zhejiang ICsprout Semiconductor Co., Ltd.) A Swing Curve Fitting Algorithm Based on Physics-Informed Neural Networks
IWAPS2025-P-33	<b>Xue Zhou<sup>1</sup>, Tianhao Huang<sup>1</sup>, Jiakun Lan<sup>1</sup>, Jianming Wu<sup>2</sup>, Yanan Bao<sup>1,2</sup>, Guodong Zhou<sup>1,*</sup></b> ( <sup>1</sup> Zhejiang University; <sup>2</sup> Zhejiang ICsprout Semiconductor Co., Ltd.) A Process-Parameter-driven Cross-Attention Convolutional Autoencoder for High-Precision Critical Dimension Prediction
IWAPS2025-P-34	<b>Baotong Chen, Biqu Liu, Xianguo Dong, Jingxun Fang, Xiaobo Guo, Yu Zhang (Shanghai Huali Integrated Circuit Co.)</b> Application of Synchrotron Radiation Multiscale Characterization Technology in Semiconductor Manufacturing Process Optimization Research
IWAPS2025-P-35	<b>Xianguo Dong, Biqu Liu, Baotong Chen, Jingxun Fang, Xiaobo Guo, Yu Zhang (Shanghai Huali Integrated Circuit Co.)</b> Challenges Faced by Carbon Spin Coating in Advanced Lithography Processes
IWAPS2025-P-36	<b>Peng Wu<sup>1</sup>, Wenliang Li<sup>1</sup>, Yiming Zhu<sup>1</sup>, Yu Fu<sup>1</sup>, Guojie Cheng<sup>2</sup>, Geffery Ying<sup>2</sup></b> ( <sup>1</sup> HuaLi Integrated Circuit Corporation, <sup>2</sup> KLA) AI Solution for Litho FS Condition Optimization
IWAPS2025-P-37	<b>Xiaomin Xu, Qian Zhang, Huanyin Xie (HuaLi Integrated Circuit Corporation)</b> Research on the Analysis and Solutions to Mask Haze in IC Manufacturing
IWAPS2025-P-38	<b>Jun-Dan Huang, Ming-Qiang Zhu (Huazhong University of Science and Technology)</b> Photoacid-induced crosslinking/degradation of poly (2,3-dihydrofuran) for multi-tone photolithography
IWAPS2025-P-39	<b>Xiao Tan<sup>1</sup>, Jun-dan Huang<sup>1</sup>, Ming-Qiang Zhu<sup>1</sup>, * (Huazhong University of Science and Technology)</b>

	Etch-Resistant Chemically Amplified Positive-Tone Photoresist with Synergistic Dual Nonionic Photoacid Generators
IWAPS2025-P-40	<b>Ying-Yi Ren<sup>1</sup>, Cong Li<sup>1</sup>, Shi-li Xiang<sup>2</sup>, Ming-Qiang Zhu<sup>1,*</sup></b> <b>(Huazhong University of Science and Technology)</b> Photoswitchable Branched Polyurethanes Based on Hexaarylbiimidazole for Photolithography

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